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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

KATO

Art Unit: 2133

Application No.: 09/494,953

Examiner: J. Torres

Filed: February 1, 2000

Attorney Dkt. No.: 108075-09034

For: TEST METHOD AND TEST CIRCUIT FOR ELECTRONIC DEVICE

AMENDMENT UNDER 37 CFR § 1.121

RECEIVED

MAR 25 2003

Commissioner for Patents
Washington, D.C. 20231

March 21, 2003

Technology Center 2100

Sir:

In reply to the outstanding Office Action dated November 21, 2002, having been duly extended from February 21, 2003, until March 21, 2003, by the attached Petition for Extension of Time, please amend the application as set forth below.

IN THE CLAIMS:

Please amend claims 11-14, 18-21 and 26-30 as follows. A copy of the marked-up original claims is attached to this response showing the changes made therein as set forth in 37 C.F.R. § 1.121.

a¹ 11. (Once Amended) An electronic device comprising first and second semiconductor devices connected to each other with a plurality of bus lines, wherein the first semiconductor device includes:

a first output circuit connected to one of the bus lines for supplying the bus line with a first logical output signal, and